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## Toggle

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A toggle is a 1-output circuit that changes state at every cycle. Design a toggle circuit that starts with the output being 0 and generates an alternating sequence of 1's and 0's, i.e., 010101010101...

### Specification

```
module toggle (t, clk, rst);
    input clk, rst;
    output t;
```

### Input

- *clk* is the clock signal.
- *rst* is the synchronous reset signal.

### Output

- *t* is the output that toggles at every cycle.

### Problem information

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Generation : 2013-09-02 15:58:59

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